

U.S. Department of Commerce, Patent and Trademark Office	Application No.: Unknown
	Filing date: May 14, 2001
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Blightman, et al.
	Group Art Unit: Unknown
"NETWORK INTERFACE DEVICE EMPLOYING A DMA COMMAND QUEUE"	Examiner name: Unknown
Express Mail No.: EF055070213US	Attorney Docket No. ALA-016

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 09/855979

U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
PB	AA	5,280,477	1/18/94	Trapp	370	85.1	
PB	AB	5,448,566	9/5/95	Richter, et al.	370	94.1	
PB	AC	5,634,127	5/27/97	Cloud, et al.	395	680	
PB	AD	5,751,715	5/12/98	Chan, et al.	370	455	
PB	AE	4,991,133	2/5/91	Davis, et al.	364	900	
PB	AF	5,163,131	11/10/92	Row, et al.	395	200	
PB	AG	5,212,778	5/18/93	Dally, et al.	395	400	
PB	AH	5,289,580	2/22/94	Latif, et al.	395	275	
PB	AI	5,303,344	4/12/94	Yokoyama, et al.	395	200	
PB	AJ	5,412,782	5/2/95	Hausman, et al.	395	250	
PB	AK	5,485,579	1/16/96	Hitz, et al.	395	200.12	
PB	AL	5,506,966	4/9/96	Ban	395	250	
PB	AM	5,511,169	4/23/96	Suda	395	280	
PB	AN	5,548,730	8/20/96	Young, et al.	395	280	

Foreign Patent Documents							
							Translation
		Document Number	Date	Country	Class	Subclass	Yes No
PB	AO	PCT/US98/08719 (WO98/50852)	11/12/98	International	—	—	
PB	AP	PCT/US97/17257 (WO98/19412)	5/7/98	International	—	—	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
PB	AQ	Internet pages entitled: DART Fast Application – Level Networking Via Data-Copy Avoidance, by Robert J. Walsh, printed 6/3/99.
PB	AR	Internet pages of InterProphet entitled: Frequently Asked Questions, by Lynne Jolitz, printed 6/14/99.
PB	AS	Internet pages entitled: Technical White Paper – Xpoint's Disk-to-LAN Acceleration Solution for Windows NT Server, printed 6/5/97.

Examiner	Date Considered 12/08/04
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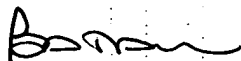
U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
PB	BA	5,588,121	12/24/96	Reddin, et al.	395	200.15	
PB	BB	5,590,328	12/31/96	Seno, et al.	395	675	
PB	BC	5,592,622	1/7/97	Isfeld, et al.	395	200.02	
PB	BD	5,634,099	5/27/97	Andrews, et al.	395	200.07	
PB	BE	5,642,482	6/24/97	Pardillos	395	200.2	
PB	BF	5,671,355	9/23/97	Collins	395	200.2	
PB	BG	5,692,130	11/25/97	Shobu, et al.	395	200.12	
PB	BH	5,699,317	12/16/97	Sartore, et al.	395	230.06	
PB	BI	5,701,434	12/23/97	Nakagawa	395	484	
PB	BJ	5,749,095	5/5/98	Hagersten	711	141	
PB	BK	5,752,078	5/12/98	Delp, et al.	395	827	
PB	BL	5,758,084	5/26/98	Silverstein, et al.	395	200.58	
PB	BM	5,758,089	5/26/98	Gentry, et al.	395	200.64	
PB	BN	5,758,186	5/26/98	Hamilton, et al.	395	831	
PB	BO	5,758,194	5/26/98	Kuzma	395	886	
PB	BP	5,790,804	8/4/98	Osborne	395	200.75	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

PB	BQ	Jato Technologies Internet pages entitled: Network Accelerator Chip Architecture, twelve-slide presentation, printed 8/19/98.
PB	BR	EETIMES article entitled: Enterprise System Uses Flexible Spec, by Christopher Harrer and Pauline Shulman, dated August 10, 1998, Issue 1020, printed 11/25/98.
PB	BS	Internet pages entitled: iReady About Us and iReady Products, printed 11/25/98.

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PB	CA	5,812,775	9/22/98	Van Seters, et al.	395	200.43	
PB	CB	5,056,058	10/8/91	Hirata, et al.	364	900	
PB	CC	5,678,060	10/14/97	Yokoyama et al.	709	212	
	CD						
	CE						
	CF						
	CG						
	CH						
	CI						
	CJ						
	CK						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

PB	CL	Internet pages entitled: Smart Ethernet Network Interface Card, which Berend Ozceri is developing, printed 11/25/98.
PB	CM	Internet pages entitled : Hardware Assisted Protocol Processing, which Eugene Feinberg is working on, printed 11/25/98.
PB	CN	Internet pages of XaQti Corporation entitled: Giga POWER Protocol Processor Product Preview, printed 11/25/98.
PB	CO	Internet pages of Xpoint Technologies www.xpoint.com web site (5 pages), printed 12/19/97.
PB	CP	Internet pages relating to iReady Corporation and the iReady Internet Tuner Module, printed 11/2/98.
PB	CQ	Internet pages entitled: Asante and 100BASE-T Fast Ethernet, printed 5/27/97.
PB	CR	Internet pages entitled: A Guide to the Paragon XP/S-A7 Supercomputer at Indiana University, printed 12/21/98.

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PB	DA	5,566,170	10/15/96	Bakke, et al.	370	60	
PB	DB	6,009,478	12/28/99	Panner, et al.	710	5	
PB	DC	6,034,963	3/07/00	Minami, et al.	370	401	
PB	DD	5,878,225	03/02/99	Bilansky et al.	395	200.57	
PB	DE	5,991,299	11/23/99	Radogna et al.	370	392	
PB	DF	5,930,830	07/27/99	Mendelson et al.	711	171	
PB	DG	6,061,368	05/09/00	Hitzelberger	370	537	
	DH						
	DI						
	DJ						
	DK						
	DL						
	DM						
	DN						

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
PB	DO	PCT/US98/14729 (WO99/04343)	1/28/99	International	—	—		
	DP							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

PB	DQ	60/053,240 (U.S. Provisional Application), by Jolitz et al. (listed filing date July 18, 1997).
PB	DR	Zilog Product Brief entitled "Z85C30 CMOS SCC Serial Communication Controller", Zilog Inc., 3 pages (1997).
PB	DS	Andrew S. Tanenbaum, Computer Networks, Third Edition, ISBN 0-13-349945-6 (1996).

Examiner <i>Barnes</i>	Date Considered 12/08/04
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U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
PB	EA	4,336,538	12/28/92	Johnson et al.	364	200	
PB	EB	5,097,442	03/17/92	Ward et al.	365	78	
PB	EC	5,629,933	05/13/97	Delp et al.	370	411	
PB	ED	5,664,114	09/02/97	Krech, Jr. et al.	395	200.64	
PB	EE	5,794,061	08/11/98	Hansen et al.	395	800.01	
PB	EF	5,802,580	09/01/98	McAlpice	711	149	
PB	EG	5,815,646	09/29/98	Purcell et al.	395	163	
PB	EH	5,771,349	06/23/98	Picazo, Jr. et al.	395	188.01	
	EI						
	EJ						
	EK						
	EL						
	EM						

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
PB	EN	PCT/US98/24943 (WO 00/13091)	11/20/98	International	—	—	—	
	EO							

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PB	EP	Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).
PB	EQ	VT8501 Apollo MVP4 Documentation, VIA Technologies, Inc., pages i-iv, 1-11, cover and copyright page, revision 1.3 (Feb. 1, 2000).
PB	ER	Internet pages entitled: Northridge/Southbridge vs. Intel Hub Architecture, 4 pages, printed 2/19/01.
PB	ES	Gigabit Ethernet Technical Brief, Achieving End-to-End Performance. Alteon Networks, Inc., First Edition, September 1996.
PB	ET	Internet pages directed to; Technical Brief on Alteon Ethernet Gigabit NIC technology, www.alteon.com, 14 pages, printed 3/15/97.

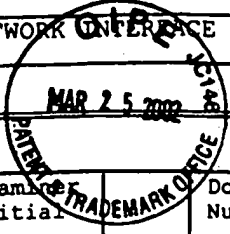
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U.S. Department of Commerce, Patent and Trademark Office						Application No.: 09/855,979	
						Filing date: March 9, 2001	
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT						Inventors: Laurence B. Boucher et al.	
						Group Art Unit: Unknown	
NETWORK INTERFACE DEVICE EMPLOYING A DMA COMMAND QUEUE						Examiner name: Unknown	
						Attorney Docket No. ALA-016	



U.S. Patent Documents							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate	
PB	AA 6,173,333	07/18/97	Jolitz et al.				
PB	AB 2001/0025315A1		Jolitz			01/10/01	
PB	AC 2001/0004354A1		Jolitz			01/10/01	
	AD						
	AE						
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	AG						
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	AI						
	AJ						
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Foreign Patent Documents							
	Document	Date	Country	Class	Subclass	Translation	
	AM					Yes	No
	AN						
	AO						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)	
AP	
AQ	
AR	
AS	

Examiner <u>Barnes</u>	Date Considered <u>12/08/04</u>
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U.S. Department of Commerce, Patent and Trademark Office

Application No.: 09/855,979

Filing date: May 14, 2001

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Inventors: Blightman, et al.

Group Art Unit: 2663

"NETWORK INTERFACE DEVICE EMPLOYING A DMA COMMAND QUEUE"

Examiner name: Unknown

Attorney Docket No. ALA-016

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
PB	AA	5,701,516	12/23/97	Cheng et al.	395	842	
PB	AB	5,809,328	09/15/98	Nogales et al.	395	825	
PB	AC	5,931,918	08/03/99	Row et al.	709	300	
PB	AD	5,935,205	08/10/99	Murayama et al.	709	216	
PB	AE	5,937,169	08/10/99	Connery et al.	395	200.8	
PB	AF	5,941,969	08/24/99	Ram et al.	710	128	
PB	AG	5,941,972	08/24/99	Hoese et al.	710	129	
PB	AH	5,950,203	09/07/99	Stakuis et al.	707	10	
PB	AI	5,996,024	11/30/99	Blumenau	709	301	
PB	AK	6,005,849	12/21/99	Roach et al.	370	276	
PB	AL	6,016,513	01/18/00	Glen H. Lowe	709	250	
PB	AM	6,026,452	02/15/00	William Michael Pitts	710	56	
PB	AN	6,044,438	03/28/00	Howard Thomas Olnowich	711	130	
PB	AO	6,047,356	04/04/00	Anderson et al.	711	129	
PB	AP	6,057,863	05/02/00	Sompong P. Olarig	345	520	
PB	AQ	6,065,096	05/16/00	Day et al.	711	114	
PB	AR	6,141,705	10/31/00	Anand et al.	710	15	
PB	AS	6,246,683	06/12/01	Connery et al.	370	392	
PB	AT	6,226,680	05/01/01	Boucher et al.	709	230	
PB	AU	6,247,060	06/12/01	Boucher et al.	709	238	
PB	AV	08/964,304		Napolitano et al.			

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Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No

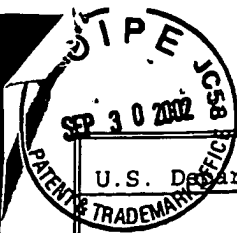
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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Foreign Patent Documents

							Translation	
PB		Document Number	Date	Country	Class	Subclass	Yes	No
PB	BA	WO 99/65219 (PCTUS99/13184)	12/16/99	International	—	—		
PB	BB	WO 01/05107 A1 (PCT US00/19006)	01/18/01	International	—	—		
PB	BC	WO 01/05116 A2 (PCT US00/19243)	01/18/01	International	—	RECEIVED		
PB	BD	WO 01/05123 A1 (PCT US00/18976)	01/18/01	International	—	OCT 01 2002		
PB	BE	WO 01/40960 A1 (PCT US00/32660)	06/07/01	International	—	Technology Center 2600		
PB	BF	WO 01/04770 A2 (PCT US00/18939)	1/18/01	International	—	—		

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

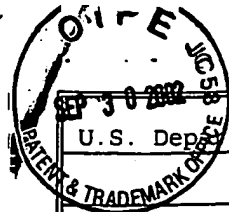
PB	BG	Web pages entitled "iReady Rounding Out Management Team With Two Key Executives", 2 pages (printed 11/28/98).
PB	BH	Toshiba, "Toshiba Delivers First Chips To Make Consumer Devices Internet-Ready Based On iReady's Design," Press Release 10/14/98, 3 pages.
PB	BI	Web pages, www.ireadyco.com , iReady, "Products," 2 pages. Printed 11/25/98.
PB	BJ	Info Work, "Toshiba, iReady shipping internet chip," by Bob Gruth, 1 page, printed 11/28/98.
PB	BK	Internet site www.interprophet.com , 17 pages, printed 3/1/00.
PB	BL	The I-1000 Internet Tuner, iReady Corporation, 2 pages date unknown.
PB	BM	Web pages from www.ireadyco.com , "Introduction," 3 pages, downloaded 11/25/98.
PB	BN	iReady New Archive, "Revolutionary Approach to Consumer Electronics Internet Connectivity Funded", San Jose, CA, 2 pages, 11/20/97.
PB	BO	iReady News Archive, "Seiko Instruments Inc. (SII) Introduces World's First Internet-Ready Intelligent LCD Modules Based on iReady Technology, by Endo, Brogan and Johnson, printed 11/2/98.
PB	BP	NEWSwatch - iReady Internet Tuner to Web Enable Devices, November 5, 1996, 2 pages, printed 11/2/98.

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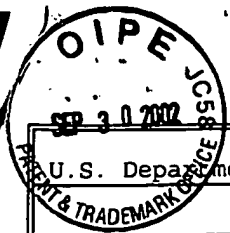
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
PB	DA	David Lammers, EETimes, 06/13/97, Tuner for Toshiba, Toshiba Taps iReady For Internet Tuner, 3 pages, printed 11/02/98.
PB	DB	Internet pages entitled: "Comparison of Novell Netware and TCP/IP Protocol Architectures," by Janique Carbone, July 16 1995, 19 pages 4/10/98.
PB	DC	Internet web pages from "Adaptec.com" website directed to the Adaptec, AEA-7110C iSCSI Host Bus Adapter... 11 pages, downloaded 10/1/01.
PB	DD	iSCSI HBA article entitled "FCE-3210/6410 32 and 64-bit PCI-to-Fibre Channed HBA", 6 pages, printed 10/01/01.
PB	DE	Internet web pages from the "iSCSI Storage .com" website that mention an Emulex HBA, 2 pages downloaded 10/01/01.
PB	DF	Internet web pages from the "iSCSI HBA.com" website that mention QLogic HBAs including the "SANblade 2300 Series", 8 pages download 10/1/01.
PB	DG	"Two-Way TCP Traffic over Rate Controlled Channels: Effects and Analysis," by Lampros Kalampoukas, Anujan Varma and K.K. Ramakrishnan, IEEE Transactions on Networking, Volume 6, Number 6, 17 pages, December 1998.
PB	DH	iReady News Archive, "Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady Design," 3 pages, downloaded November 2, 1998.
PB	DI	"File System Design for an NFS File Server Appliance," by Dave Hitz, et al., 13 pages, copyright 1996.
PB	DJ	Adaptec press release, "Adaptec Announces EtherStorage Technology", 2 pages May 4, 2000, printed 6/14/00.
PB	DK	Adaptec article entitled, "EtherStorage Frequently Asked Questions," 5 pages, downloaded July 19, 2000.
PB	DL	Adaptec article entitled, "EtherStorage White Paper," 7 pages, downloaded July 19, 2000.
PB	DM	Article entitled, "Computers; Storage," by James Berlino and Ananda Baruah, CIBC World Markets Equity Research, 9 pages, August 7, 2000.
PB	DN	Merrill Lynch article entitled, "Storage Futures," by Steven Milunovich, 22 pages, May 10, 2000.
PB	DO	CBS Market Watch article entitled "Montreal start-up battles data storage bottleneck," by Susan Taylor, 2 pages, dated March 5, 2000, 2 pages printed 3/7/00.
PB	DP	Internet-Draft article entitled "SCSI/TCP (SCSI over TCP)," by Satran et al., 38 pages, dated February 2000, printed 5/19/00.
Examiner <i>B. Tan</i>		Date Considered 12/08/04
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		Document Number	Date	Country	Class	Subclass	

OTHER ART (Including Author, title, Date, Pertinent pages, Etc.)

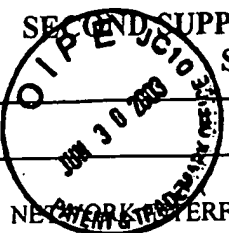
PB	EA	Form 10-K for Exelan, Inc., for the fiscal year ending December 31, 1987 (10 Pages).
PB	EB	Form 10-K for Exelan, Inc., for the fiscal year December 31, 1988 (10 pages).
PB	EC	"Second Supplemental Information Disclosure Statement per 37 C.F.R. §1.97(i)", dated July 29, 2002 relating to Exelan Inc. as submitted in Application Serial No. 09/464,283
PB	ED	WindRiver article entitled "Tornado: For Intelligent Network Acceleration", copyright 2001, 2 pages.
PB	EE	WindRiver White Paper entitled "Complete TCP/IP Offload for High-Speed Ethernet Networks", Copyright 2002, 7 pages.
PB	EF	Intel article entitled "Solving Server Bottlenecks with Intel Server Adapters", Intel Corporation, Copyright 1999, 8 pages.

Examiner <i>B...</i>	Date Considered 12/08/04
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U.S. Department of Commerce, Patent and Trademark Office	Application No.: 09/855,979
SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Filing date: May 14, 2001
	Inventors: Stephen E. J. Blightman, et al.
	Group Art Unit: 2663
	Examiner name: Unknown
	Attorney Docket No. ALA-016



NETWORK INTERFACE DEVICE EMPLOYING A DMA COMMAND QUEUE

U.S. Patent Documents

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PB	A	5,058,110	10/15/91	Beach et al.	370	85.6
PB	B	6,021,446	02/01/00	Gentry, Jr.	709	303
PB	C	6,356,951	03/12/02	Gentry, Jr.	709	250
PB	D	6,389,468	05/14/02	Muller et al.	709	226
PB	E	6,427,169	07/30/02	Elzur	709	224
PB	F	6,434,651	08/13/02	Gentry, Jr.	710	260
PB	G	6,449,656	09/10/02	Elzur et al.	709	236
PB	H	6,453,360	09/17/02	Muller et al.	709	250
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Foreign Patent Documents

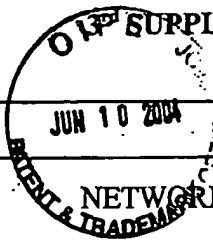
							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	L							
	M							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

PB	N	Article from Rice University entitled "LRP: A New Network Subsystem Architecture for Server Systems", by Peter Druschel and Gaurav Banga, Rice University, October 1996, 8 pages.
PB	O	Internet RFC/STD/FYI/BCP Archives article with heading "RFC2140" entitled "TCP Control Block Interdependence", web address http://www.faqs.org/rfcs/rfc2140.html , 9 pages, printed 9/20/02.
	P	
	Q	

Examiner	Date Considered 12/08/04
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***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

U.S. Department of Commerce, Patent and Trademark Office	Application No.: 09/855,979
 <p>SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p>	Filing date: May 14, 2001
	Inventors: Stephen E. J. Blightman, et al.
<p>NETWORK INTERFACE DEVICE EMPLOYING A DMA COMMAND QUEUE</p>	Group Art Unit: 2663
	Examiner name: Unknown
	Attorney Docket No. ALA-016

U.S. Patent Documents							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate	
PB	A	5,598,410	1/28/1997	Stone	370	469	
	B						
	C						
	D						
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							Translation
	Document Number	Date	Country	Class	Subclass	Yes	No
	L						
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Examiner <u>BATMAN</u>	Date Considered <u>12/08/04</u>
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U.S. Department of Commerce, Patent and Trademark Office <div style="position: absolute; left: 0; top: 50px; border: 1px solid black; border-radius: 50%; padding: 5px; transform: rotate(-45deg); width: 100px; text-align: center;"> OIPE 4th JUN 24 2004 PATENT & TRADEMARK OFFICE </div> SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT NETWORK INTERFACE DEVICE EMPLOYING A DMA COMMAND QUEUE	Application No.: 09/855,979 Filing date: May 14, 2001 Inventors: Stephen E. J. Blightman, et al. Group Art Unit: 2663 Examiner name: Unknown Attorney Docket No. ALA-016
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U.S. Patent Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
PS	A	5,517,668	5/14/1996	Szwerinski et al.	395	800
	B					
	C					
	D					
	E					
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	J					
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Foreign Patent Documents

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		Document Number	Date	Country	Class	Subclass	Yes	No
	L							
	M							

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Examiner <i>PS</i>	Date Considered 12/08/04
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